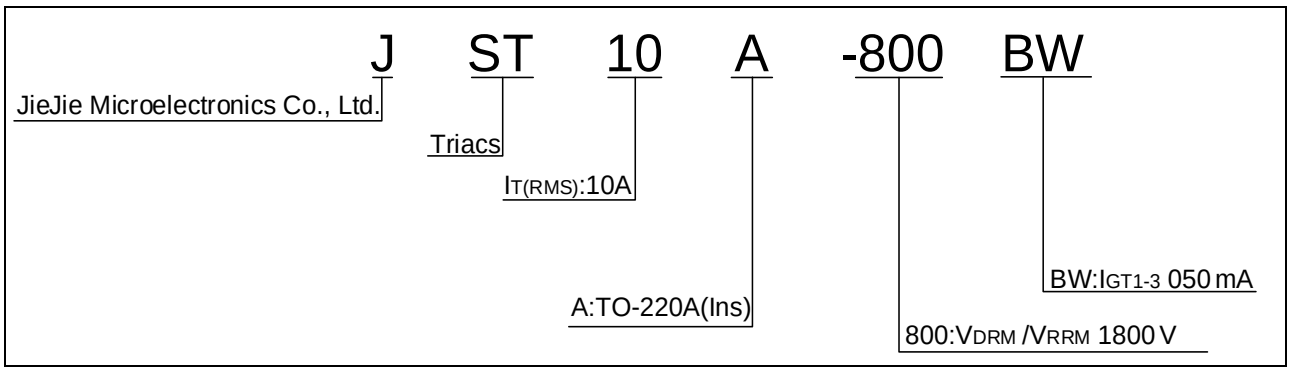
Peak gate power	P_{GM}	10	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	4.5	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value	Unit
I_{GT}	V_D =12V R_L =33	-		

ORDERING INFORMATION



MARKING

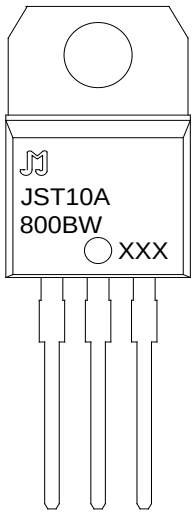


FIG.1 Maximum power dissipation versus RMS on-state current

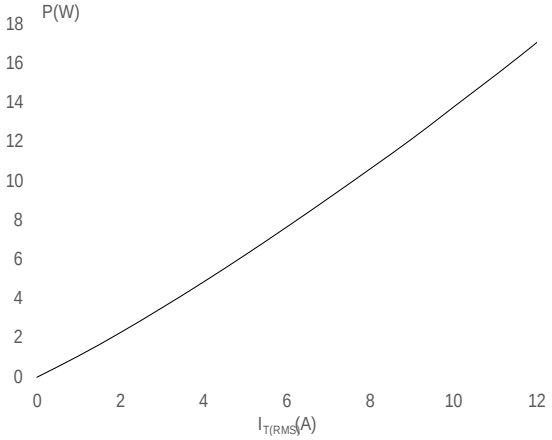


FIG.2: RMS on-state current versus case temperature

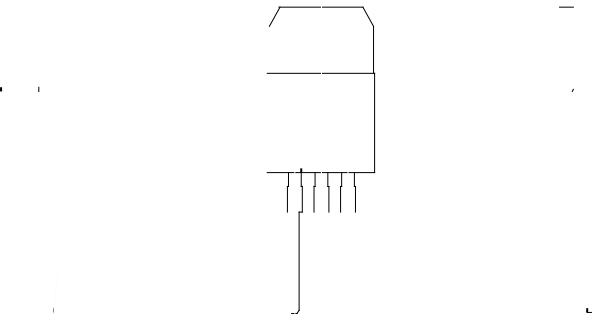
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
JST10A-800BW	800	50	TO-220A(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated

PACKAGE MECHANICAL DATA



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